AI for System and System for AI

Conless Pan[†]

July 27, 2023

[†]ACM Class 2022 Shanghai Jiao Tong University









Introduction



The Rise of Machine Learning

New chat		4 GPT-35 +* GPT-4 ↔	
day		, and a	
ACM Growth: Change & Efficie			
vicus 7 Days		Oh HODT	
Upbeat Study Buddies		ChatGPT	
□ 提取HTML版本信息			
Shell Usage: \$() and =	-j¢;-	47	\triangle
提取网址至输入文件	Examples	Capabilities	Limitations
Shell Task: Summarize Reques	"Explain quantum computing in simple terms" →	Remembers what user said earlier in the conversation	May occasionally generate incorrect information
正则表达式概述。	*Oot any creative ideas for a 10	Allows upor to provide follow up	May accasionally produce
lew chat	year old's birthday?" →	corrections	harmful instructions or biased
APP观察脚本编写 (4 words)	"How do I make an HTTP request	Trained to decline inappropriate	
us 30 Days	in Javascript?" →	requests	Limited knowledge of world and events after 2021
Data Structures for CS			
New chat			
Financial Aid Application Requ			
A*在自动驾驶中的重要性			
CPU温度解决方案			
环法自行车赛结果未知			
提取微信文章至Word			
Upgrade to Plus NEW	Send a message		
conlesspan@outlook.c	Free Research Preview. ChatGPT may p	roduce inaccurate information about people, pla	aces, or facts. ChatGPT July 20 Version

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• What is machine learning?

- What is machine learning?
- Do you know its history?

- What is machine learning?
- Do you know its history?
- Why is it so important today?

- What is machine learning?
- Do you know its history?
- Why is it so important today?

But I don't want to talk about them, cause I've got no interest in AI.

• Generative AI

- Generative AI
- Al for science

- Generative AI
- Al for science
- Others

AI for System

Let's compare these two games:¹

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¹James E Smith. "A study of branch prediction strategies". In: 25 years of the international symposia on Computer architecture (selected papers). 1998, pp. 202–215.

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And it turns out that...²³

July 27, 2023



 $^{^2}$ Anastasios Zouzias, Kleovoulos Kalaitzidis, and Boris Grot. "Branch Prediction as a Reinforcement Learning Problem: Why, How and Case Studies". In: arXiv preprint arXiv:2106.13429 (2021).

³David Silver et al. "Mastering the game of go without human knowledge". In: nature 550.7676 (2017), pp. 354-359.

And it turns out that...²³

$\exists r \times iV > cs > arXiv:2106.13429$

Help | Advance

Computer Science > Machine Learning

(Submitted on 25 Jun 2021)

Branch Prediction as a Reinforcement Learning Problem: Why, How and Case Studies

Anastasios Zouzias, Kleovoulos Kalaitzidis, Boris Grot

Recent years have seen stagnating improvements to branch predictor (ØP) efficary and a dearth of fresh likes in branch predictor desips, calling for thesh thinking in this area. This paper argues that looking at BP from the viewpoint of Reinforcement Learning (RU) facilitates systematic reasonization of, BP designs, We desirche havo to apply the RL formulation to branch predictors, show that existing predictors can be succinctly expressed in this formulation, as study to Re L3-asset variants of conventional IBPs.

Commerci: € pages, appared in ML workshop for Computer Architecture and Systems 2021 Subjects: ML-Internation (Sci.0) Artifical Intelligence (cs.0) Cite as: arXiv:2106.13429 (cs.16) for arXiv:2106.13429 (cs.16) https://doi.org/10.48550/arXiv.2106.13429 €

²Anastasios Zouzias, Kleovoulos Kalaitzidis, and Boris Grot. "Branch Prediction as a Reinforcement Learning Problem: Why, How and Case Studies". In: *arXiv preprint arXiv:2106.13429* (2021).

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Computer Science > Machine Learning

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Comments: 6 pages, appeared in ML workshop for Computer Architecture and Systems 2021 Subjects: Machine Learning (cs.LG), Artificial Intelligence (cs.Al) Teas: anXiv:2106.13429 [cs.LG] for arXiv:2106.13429 [cs.LG] for this version https://doi.org/10.45520/arXiv.2106.13429]

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Published: 19 October 2017

Mastering the game of Go without human knowledge

David Silver ⊡, Julian Schrittwieser, Karen Simonyan, Ioannis Antonoglou, Aja Huang, Arthur Guez, Thomas Hubert, Lucas Baker, Matthew Lai, Adrian Bolton, Yutian Chen, Timothy Lillicrap, Fan Hui, Laurent Silre, George van den Driessche, Thore Graepel & Demis Hassabis

Nature 550, 354-359 (2017) Cite this article

345k Accesses | 4205 Citations | 2528 Altmetric | Metrics

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Decision-making problems

- Decision-making problems
- A finite state given as input

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- A finite state given as input
- A simple decision required as output

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- A finite state given as input
- A simple decision required as output

The role that Al plays: looking for a fitting function y = f(x), which calculates the correct/best y with given x.

The perceptron was introduced at first:⁴

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⁴Daniel A Jiménez and Calvin Lin. "Dynamic branch prediction with perceptrons". In: Proceedings HPCA Seventh International Symposium on High-Performance Computer Architecture. IEEE. 2001, pp. 197–206.

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Figure 1: Perceptron Model. The input values $x_1, ..., x_n$, are propagated through the weighted connections by taking their respective products with the weights $w_1, ..., w_n$. These products are summed, along with the bias weight w_0 , to produce the output value y.

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3 9/30

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How does AI perform its job?

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⁴Daniel A Jiménez and Calvin Lin. "Dynamic branch prediction with perceptrons". In: Proceedings HPCA Seventh International Symposium on High-Performance Computer Architecture. IEEE. 2001, pp. 197–206.

AMD Zen microarchitecture, the start of "AMD YES".

Front End [edit]

The Front End of the Zen core deals with the in-order operations such as instruction fetch and instruction decode. The instruction fetch is composed of two paths: a traditional decode path where instructions come from the instruction cache and a µOPs cache that are determined by the branch prediction (BP) unit. The instruction stream and the branch prediction unit track instructions in 64B windows. Zen is AMD's first design to feature a µOPs cache, a unit that not only improves performance, but also saves power (the µOPs cache was first introduced by Intel in their Sandy Bridge microarchitecture).



tuned. AMD stated it's also larger than previous architectures but did not disclose actual sizes. Once the BP detects an indirect target operation, the branch is moved to the Indirect Target Array (ITA) which is 512 entry deep. The BP includes a 32-entry return stack.

In Zen, AMD moved the instruction TLB to BP (to much earlier in the pipeline than in previous architectures). This was done to allow for more-aggressive prefetching by allowing the physical address to be retrieved at an earlier stage. The BP is capable of storing 2 branches per BTB (Branch Target Buffer) entry, reducing the number of BTB reads necessary. ITLB is composed of:



Advanced neural network is also being introduced to many subfields of computer system...

Let's go over the basic concept of rl.

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Reinforcement Learning

• A virtual agent who makes decisions

Let's go over the basic concept of rl.

- A virtual agent who makes decisions
- A state space $S = \{s_i\}$

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- An action space $A = \{a_i\}$
- Rewards r_{ai|si}

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Reinforcement Learning

- A virtual agent who makes decisions
- A state space $S = \{s_i\}$
- An action space $A = \{a_i\}$
- Rewards r_{ai|si}

When agent receives a reward or a feedback, it updates the estimation

$$\mathbb{E}(r_{a_i|s_i})$$

or under some situation the probability

$$\mathbb{P}(r_{a_i|s_i} > 0)$$

The problems that reinforcement learning can deal with:

- Heuristic
- Empirical

Database Management System

The configuration of the knobs⁵

	Category	Functionality	Example (Postgres)	Example (MySQL)
1 Access Control	Connections	max_connections	innodb_thread_concurrency	
	1 Access Control	Transactions	deadlock_timeout	innodb_table_locks
2	2 Query Optimizer	Query Plan	join_collapse_limit	rewriter_enabled
-		Cost Values	seq_page_cost	join_buffer_size
3	Query Executor	Persistence	full_page_writes	replica_pending_jobs_size_max
4 Background Processes	Background Processes	Logging	log_rotation_size	binlog_cache_size
	background riocesses	Others	checkpoint_timeout	innodb_log_file_size
5	Resource (CPU)	CPU Usage	max_files_per_process	innodb thread concurrency
6	Resource (Memory)	Memory Space	shared_buffers	innodb_buffer_pool_size
7	Resource (Disk)	Disk IO/Caches	temp_file_limit	max_sort_file_size

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Co

in 🤨 f 🖬 Automatic Database Management System Tuning Through Large-scale Machine Learning

Authors: 📀 Dana Van Aken, 🕘 Andrew Pavlo, 🕃 G	eoffrey J. Gordon, 🅘 Bohan Zhang Authors Info & Claims	
SIGMOD '17: Proceedings of the 2017 ACM International G • https://doi.org/10.1145/3035918.3064029	Conference on Management of Data • May 2017 • Pages 1009–1024	
Published: 09 May 2017 Publication History	Check for updates	
99 239 🔊 7,413	1 E 11 Gereader PDF	
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The management of page table index⁶

Virtual Address Translation via Learned Page Table Indexes

Artemiy Margaritov[†] Dmitrii Ustiugov[‡] Edouard Bugnion[‡] Boris Grot[†]
[†]University of Edinburgh [‡]EPFL

Abstract

Address translation is an established performance bottleneck [4] in workloads operating on large datasets due to frequent TLB misses and subsequent page table walks that often require multiple memory accesses to resolve. Inspired by recent research at Google on Learned Index Structures [12], we propose to accelerate address translation by introducing a new translation mechanism based on learned models using neural networks. We argue that existing software-based learned models using hole to outperform the traditional address translation mechanism due to their high inference time, pointing toward the need for hardware-accelerated learned models. With a challenging goal to microarchitect a hardware-friendly learned page table index, we discuss a number of machine learning and systems trade-offs, and suggest future directions.

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⁶Artemiy Margaritov et al. "Virtual address translation via learned page table indexes". In: Conference on Neural Information Processing Systems. 2018.

Anywhere we use heuristic to make a decision can be replaced by ml!⁷

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⁷ Jeff Dean. "Machine learning for systems and systems for machine learning". In: Presentation at 2017 Conference on Neural Information Processing Systems. 2017.

Anywhere we use heuristic to make a decision can be replaced by ml!⁷

- Compilers: instruction scheduling, register allocation, loop nest parallelization strategies, ...
- Networking: TCP window size decisions, backoff for retransmits, data compression, ...
- Operating systems: process scheduling, buffer cache insertion/replacement, file system prefetching, ...
- Job scheduling systems: which tasks/VMs to co-locate on same machine, which tasks to pre-empt, ...
- ASIC design: physical circuit layout, test case selection, ...

⁷ Jeff Dean. "Machine learning for systems and systems for machine learning". In: Presentation at 2017 Conference on Neural Information Processing Systems. 2017.

There are some frameworks for these systems⁸:

Park: An Open Platform for Learning-Augmented Computer Systems

Part of Advances in Neural Information Processing Systems 32 (NeurIPS 2019)

AuthorFeedback	Bibtex	MetaReview	Metadata	Paper	Reviews	Supplemental

Authors

Hongzi Mao, Parimarjan Negi, Akshay Narayan, Hanrui Wang, Jiacheng Yang, Haonan Wang, Ryan Marcus, ravichandra addanki, Mehrdad Khani Shirkoohi, Songtao He, Vikram Nathan, Frank Cangialosi, Shaileshh Venkatakrishnan, Wei-Hung Weng, Song Han, Tim Kraska, Dr.Mohammad Alizadeh

Abstract

We present Park, a platform for researchers to experiment with Reinforcement Learning (RL) for computer systems. Using RL for improving the performance of systems has a lot of potential, but is also in many ways very different from, for example, using RL for games. Thus, in this work we first discuss the unique challenges RL for systems has, and then propose Park an open extensible platform, which makes it easier for ML researchers to work on systems problems. Currently, Park consists of 12 real world system-centric optimization problems with one common easy to use interface. Finally, we present the performance of existing RL approaches over those 12 problems and outline potential areas of future work.

⁸Hongzi Mao et al. "Park: An open platform for learning-augmented computer systems". In: Advances in Neural Information Processing Systems 32 (2019).

System for AI

How can our works on system promote research of AI?

Large models are conquering ml...



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• Low speed

- Low speed
- Limited memory

- Low speed
- Limited memory
- Popularization

Speed can be achieved with loss of precision and generality



Thus we have Nvidia GPUs with CUDA units and tensor cores, Google TPU and Apple NPU.







In traditional data storage and computing, we distribute data/tasks to multiple machines for larger storage size and better speed.



• Distributed machine learning system (parallel compute / parallel data)

- Distributed machine learning system (parallel compute / parallel data)
- Computing device placement

- Distributed machine learning system (parallel compute / parallel data)
- Computing device placement
- Other topics in traditional distributed sys (communication, consistency)

Programmers don't want to build their projects from every detail.





When writing cpp programs, it's convenient for us to use those frameworks and libraries.

Programmers don't want to build their projects from every detail.







When writing cpp programs, it's convenient for us to use those frameworks and libraries.

Al programmers also need them.



Frameworks

```
cnn.py > ...
     from torch import nn
     from torch.nn import functional as func
 4
5
     class CNN(nn.Module):
       def __init__(self):
         super(CNN, self). init ()
         self.conv1 = nn.Conv2d(1, 32, kernel_size=3, stride=1, padding=1)
8
9
         self.pool = nn.MaxPool2d(2, 2)
10
         self.conv2 = nn.Conv2d(32, 64, kernel size=3, stride=1, padding=1)
         self.fc1 = nn.Linear(64 * 7 * 7, 1024)
11
12
         self.fc2 = nn.Linear(1024, 512)
13
         self.fc3 = nn.Linear(512, 10)
14
15
       def forward(self, x):
16
         x = self.pool(func.relu(self.conv1(x)))
17
         x = self.pool(func.relu(self.conv2(x)))
18
         x = x.view(-1, 64 * 7 * 7)
19
         x = func.relu(self.fc1(x))
20
         x = func.relu(self.fc2(x))
         x = self.fc3(x)
22
         return x
23
24
     net = CNN()
```

Tiny ML

Every personal device should be able to train a model⁹

Large Neural Networks





Small Neural Networks



Low-Power Hardware

Model Compression & TinyML



⁹Song Han. "Efficient Deep Learning Computing: Model Compression and Acceleration".



Prospects



The Case for Learning-and-System Co-design

Chieh-Jan Mike Liang, Hui Xue, Mao Yang, Lidong Zhou ACM SIGOPS Operating Systems Review | July 2019, Vol 53(1): pp. 68-74

While decision-makings in systems are commonly solved with explicit rules and heuristics, machine learning (ML) and deep learning (DL) have been driving a paradigm shift in modern system design. Based on our decade of experience in operationalizing a large production cloud system, Web Search, learning fills the gap in comprehending and taming the system design and operation complexity. However, rather than just improving specific ML/DL algorithms or system features, we posit that the key to unlocking the full potential of learning-augmented systems is a principled methodology promoting learning-and-system co-design. On this basis, we present the AutoSys, a common framework for the development of learning-augmented systems.

• Should be both good AI and systems research

- Should be both good AI and systems research
 - Provides insights to both communities

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 - Provides insights to both communities
- Leverages understanding of both domains

- Should be both good AI and systems research
 - Provides insights to both communities
- Leverages understanding of both domains
- I don't like adjust those parameters

Thank you!